

714	713	Subclass
ISSUE CLASSIFICATION		

U.S. UTILITY Patent Application

12W SCANNED <i>AS</i> <i>[Signature]</i> O.A.	O.I.P.E.	PATENT DATE
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APPLICATION NO. 09/664910	CONT/PRIOR.	CLASS 710 714	SUBCLASS 718	PART UNIT 2132 2133	EXAMINER Dipakkumar Gandhi
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APPLICANTS

4. Memory module and memory component built-in self test

PTO-2040
12/89

ISSUING CLASSIFICATION											
ORIGINAL				CROSS REFERENCE(S)							
CLASS		SUBCLASS		CLASS		SUBCLASS (ONE SUBCLASS PER BLOCK)					
7114		7118		7114		7135					
INTERNATIONAL CLASSIFICATION				365		2011					
G 1116		297/69									

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. <div style="text-align: center;">2</div>	Figs. Drwg. <div style="text-align: center;">2</div>	Print Fig. <div style="text-align: center;">2</div>	Total Claims <div style="text-align: center;">64</div>	Print Claim for O.G. <div style="text-align: center;">59</div>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	Dipakkumar Gandhi 12/24/03 (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	ALBERT DECADY SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2100 A. Decady 12/29/03 (Primary Examiner) (Date)			12-20-03 ISSUE FEE	
				Amount Due <div style="text-align: center;">\$1330</div>	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	V. J. J. 8-12-04 (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	
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